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7. The flat panel display of claim 1, further comprising a material for a light emission layer arranged on a portion of the active area.

8. The flat panel display of claim 1, further comprising:  
a thin film transistor with a source electrode and a drain electrode,  
wherein the source electrode or the drain electrode is  
coupled with the first electrode layer.

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9. The flat panel display of claim 1, wherein the repellant area is positioned around the active area.

10. The flat panel display of claim 1, further comprising a second electrode layer arranged on the active area.

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